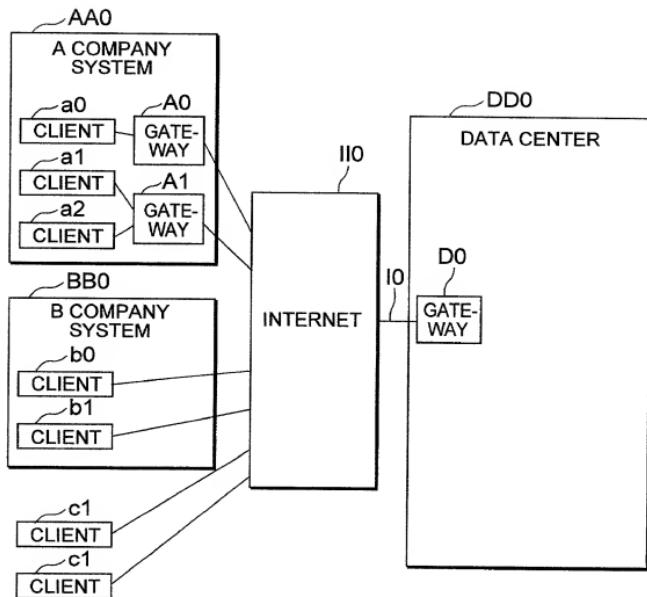


FIG. 1

TOSICO20 = 62676360



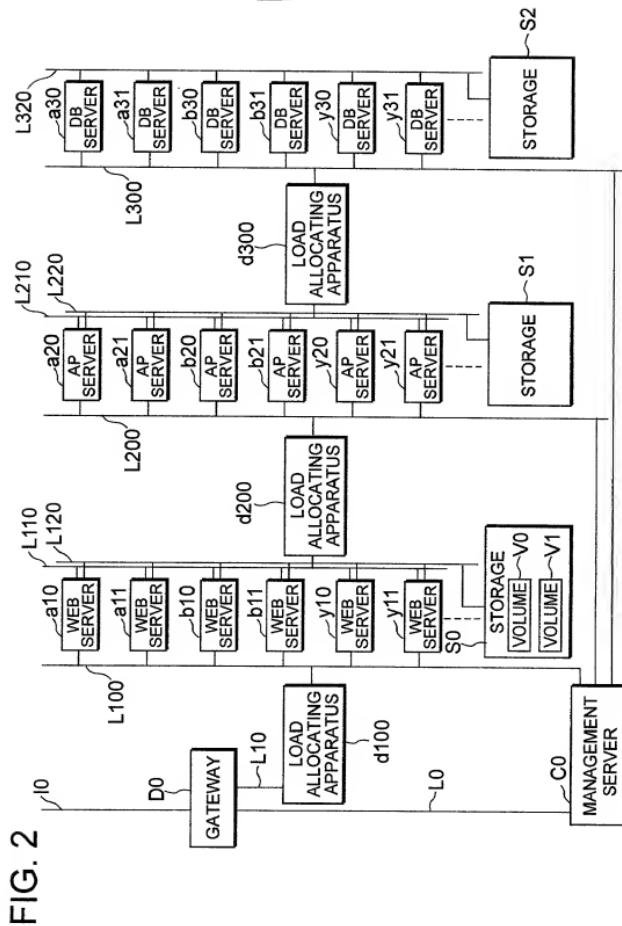
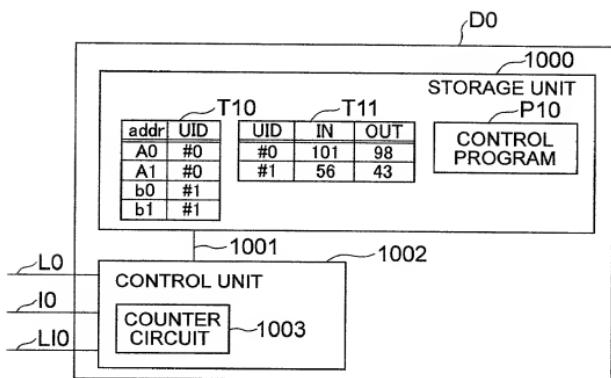


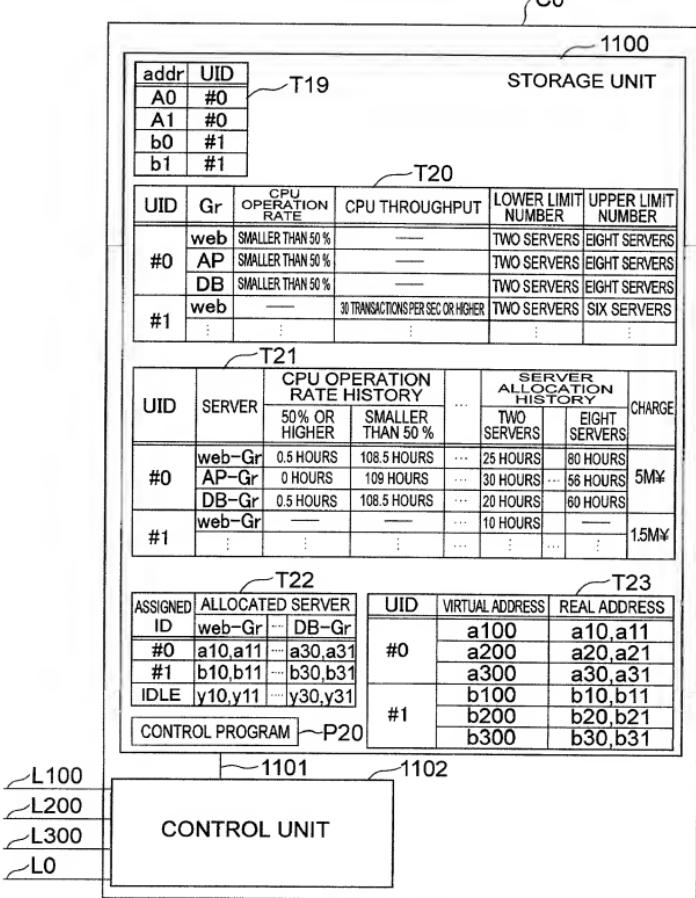
FIG. 2

FIG. 3



TOSO/CD: 62676860

FIG. 4



Tamaki *et al.*

FIG. 5

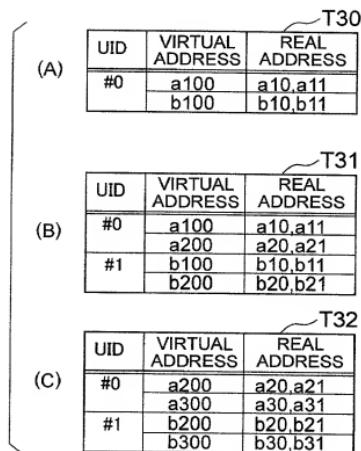


Diagram illustrating three tables (A, B, C) representing memory mappings. Each table has three columns: UID, VIRTUAL ADDRESS, and REAL ADDRESS. The tables are grouped by a vertical bracket on the left and labeled T30, T31, and T32 above them.

UID	VIRTUAL ADDRESS	REAL ADDRESS
#0	a100 b100	a10,a11 b10,b11

(A)

UID	VIRTUAL ADDRESS	REAL ADDRESS
#0	a100 a200	a10,a11 a20,a21
#1	b100 b200	b10,b11 b20,b21

(B)

UID	VIRTUAL ADDRESS	REAL ADDRESS
#0	a200 a300	a20,a21 a30,a31
#1	b200 b300	b20,b21 b30,b31

(C)

FIG. 6

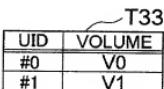
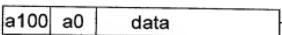


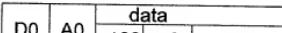
Diagram illustrating a table (T33) representing volume assignments. The table has two columns: UID and VOLUME.

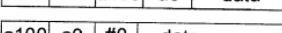
UID	VOLUME
#0	V0
#1	V1

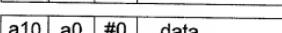
T33

FIG. 7

(A)  ~1200

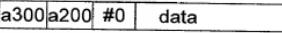
(B)  ~1201

(C)  ~1202

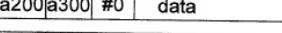
(D)  ~1203

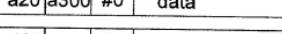
(E)  ~1204

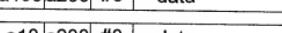
(F)  ~1205

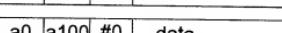
(G)  ~1206

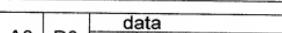
(H)  ~1207

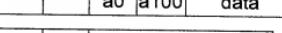
(I)  ~1208

(J)  ~1209

(K)  ~1210

(L)  ~1211

(M)  ~1212

(N)  ~1213

(O)  ~1214

FIG. 8

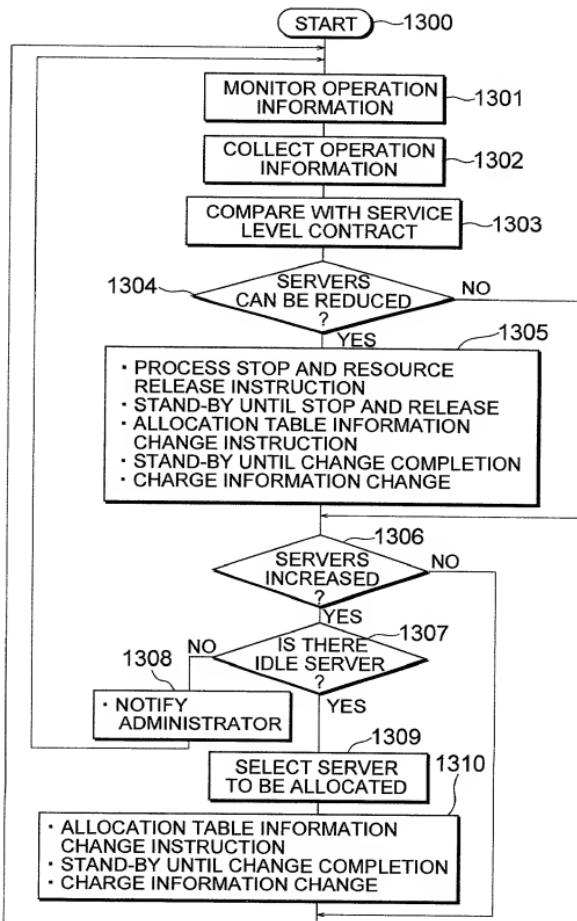
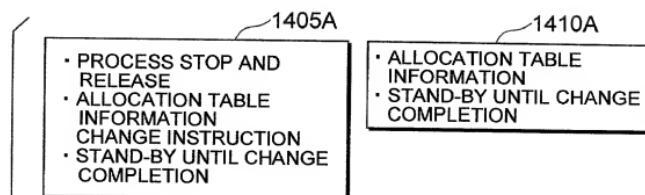
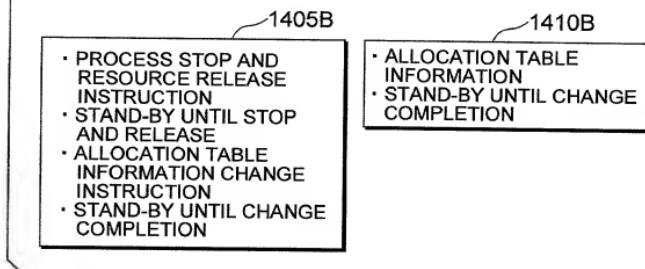


FIG. 9

(A)



(B)



005000-02676860

FIG. 10

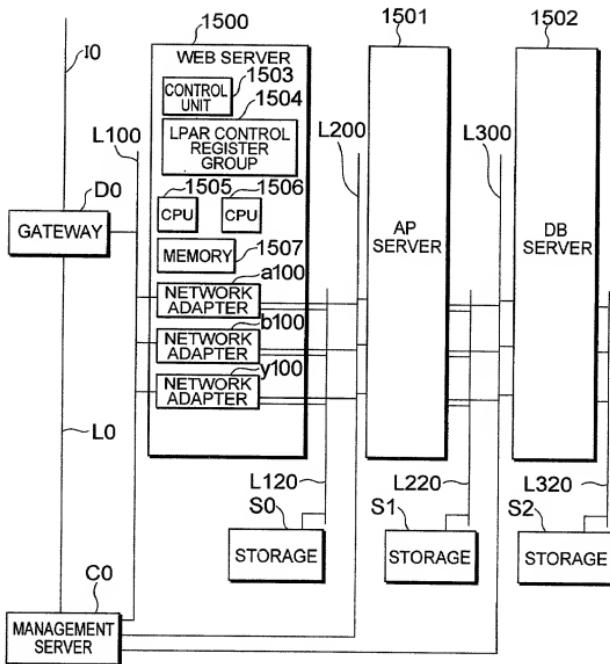


FIG. 11

T40

LPAR#	UID	ADAPTER	MEMORY ALLOCATION START OFFSET	SIZE	CPU ALLOCATION%
#0	#0	a100	0	2GB	50%
#1	#1	b100	2GB	2GB	20%
#2	not assign	y100	4GB	2GB	disable

FIG. 12

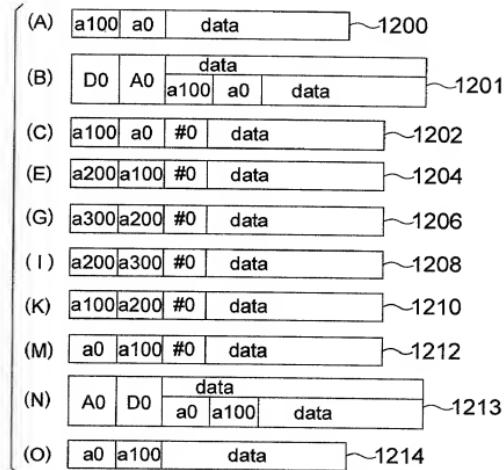


Diagram illustrating memory structures A through O, each consisting of a header and a data field. The header fields are represented by boxes, and the data field is a long horizontal box. The structures are grouped by a vertical brace on the left.

(A)	a100	a0	data	~1200			
(B)	D0	A0	data	a100	a0	data	~1201
(C)	a100	a0	#0	data	~1202		
(E)	a200	a100	#0	data	~1204		
(G)	a300	a200	#0	data	~1206		
(I)	a200	a300	#0	data	~1208		
(K)	a100	a200	#0	data	~1210		
(M)	a0	a100	#0	data	~1212		
(N)	A0	D0	data	a0	a100	data	~1213
(O)	a0	a100	data	~1214			

FIG. 13

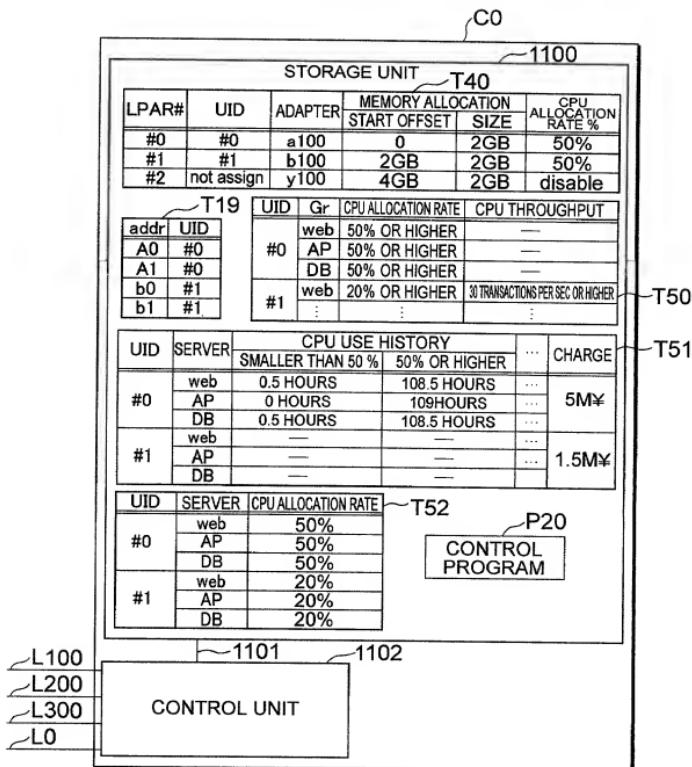
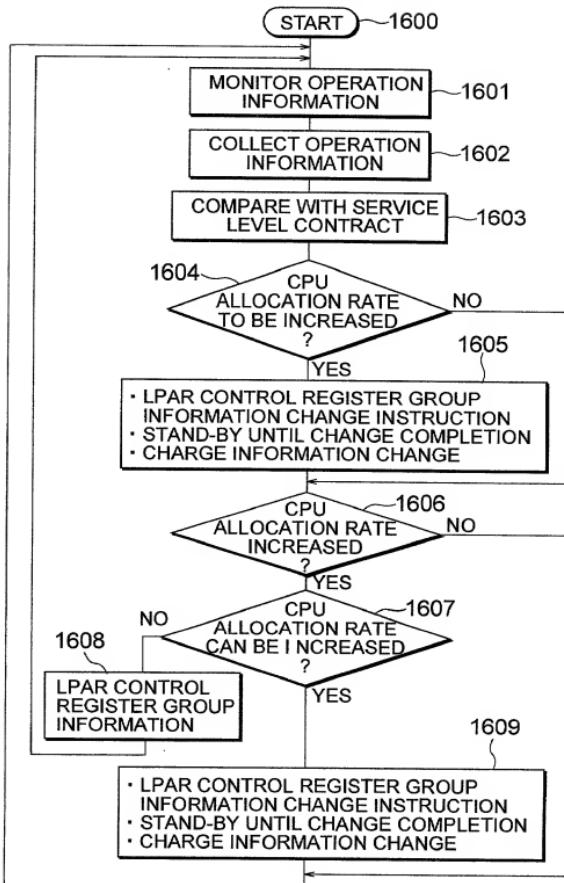


FIG. 14



Tamaki *et al.*

FIG. 15

TOS020 = 62626860

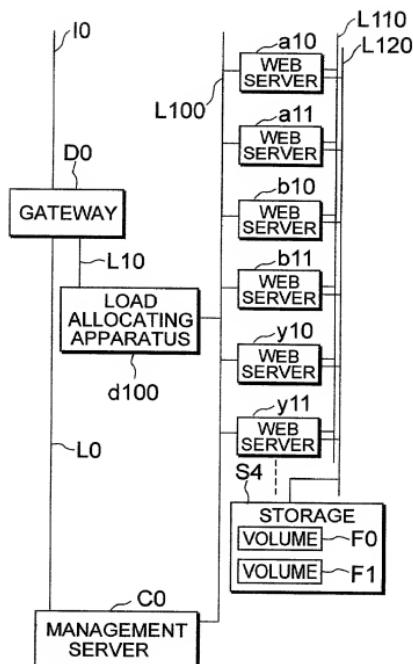


FIG. 16

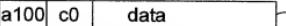
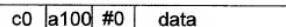
(A)  ~1700  
 (C)  ~1702  
 (D)  ~1703  
 (M)  ~1712  
 (O)  ~1714

FIG. 17

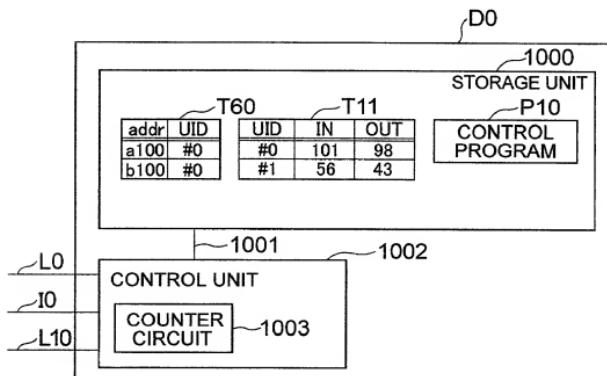


FIG. 18

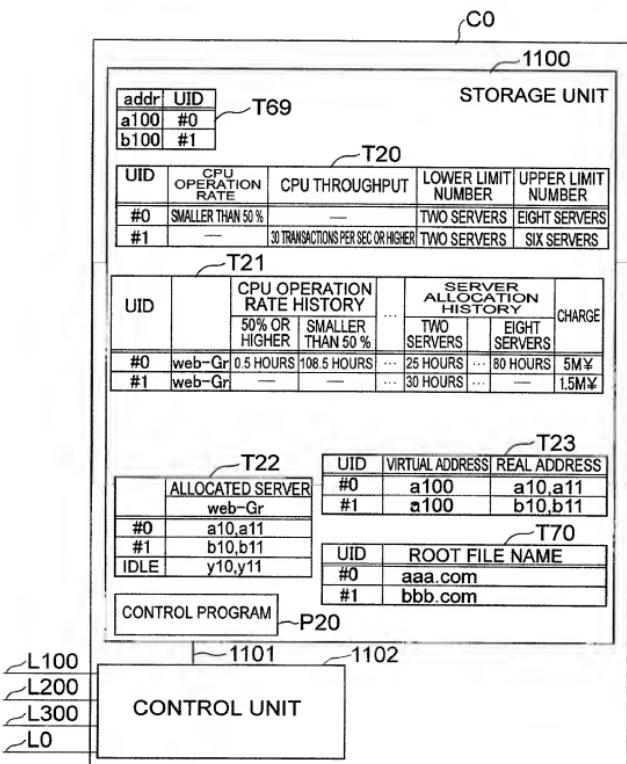


FIG. 19

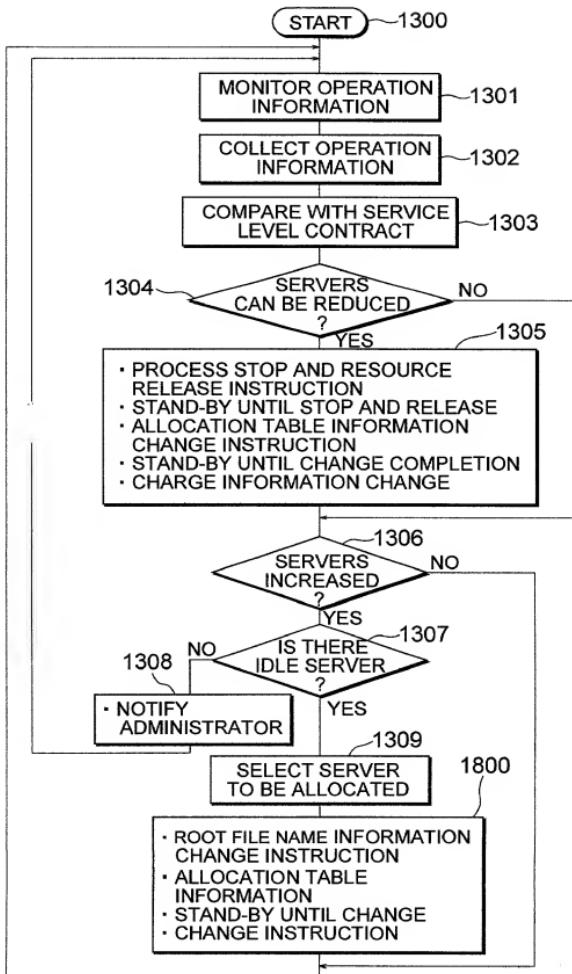


FIG. 20

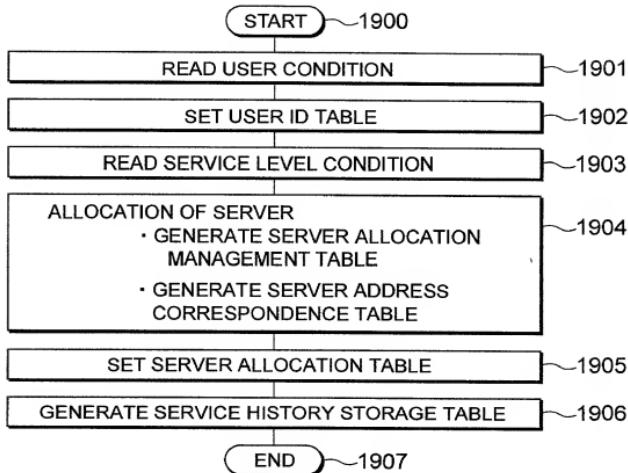


FIG. 21

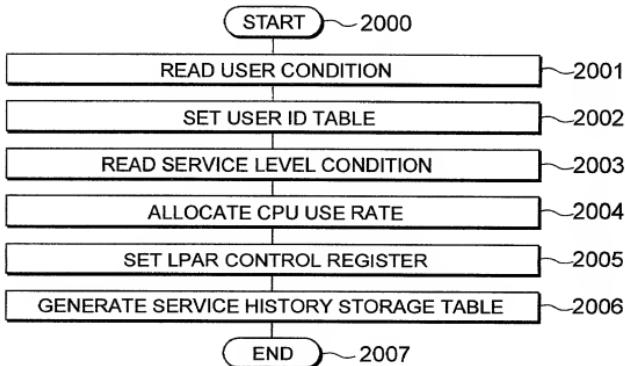


FIG. 22

2100

USER CONDITION INPUT DIALOG

USER IDENTIFICATION CONDITION

ACCESS SOURCE IP ADDRESS	<input checked="" type="checkbox"/>	A0,A1
ACCESS DESTINATION IP ADDRESS	<input type="checkbox"/>	
...	<input type="checkbox"/>	

SERVER TYPE      IP ADDRESS

WEB SERVER	<input checked="" type="checkbox"/>	a100
AP SERVER	<input checked="" type="checkbox"/>	a200
DB SERVER	<input checked="" type="checkbox"/>	a300

FIG. 23

2200

SERVICE LEVEL CONDITION INPUT DIALOG

SERVER TYPE	NUMBER OF SERVERS		CPU OPERATION RATIO	USE CONDITION	...
	LOWER LIMIT	UPPER LIMIT			
WEB SERVER	<input checked="" type="checkbox"/>	2 SERVERS	8 SERVERS	SMALLER THAN 50 %	
AP SERVER	<input checked="" type="checkbox"/>	2 SERVERS	8 SERVERS	SMALLER THAN 50 %	
DB SERVER	<input checked="" type="checkbox"/>	2 SERVERS	8 SERVERS	SMALLER THAN 50 %	

REQUESTED PERFORMANCE

OUTPUT TRANSACTION THROUGHPUT	<input type="checkbox"/>	
OUTPUT/INPUT THROUGHPUT RATIO	<input type="checkbox"/>	
AVERAGE TRANSACTION PROCESS LATENCY	<input type="checkbox"/>	

FIG. 24

2300

SERVICE LEVEL CONDITION INPUT DIALOG

SERVER TYPE	CPU ALLOCATION RATE
WEB SERVER	<input checked="" type="checkbox"/> 50 % OR HIGHER
AP SERVER	<input checked="" type="checkbox"/> 50 % OR HIGHER
DB SERVER	<input checked="" type="checkbox"/> 50 % OR HIGHER

REQUESTED PERFORMANCE

OUTPUT TRANSACTION THROUGHPUT	<input type="text"/>	<input type="text"/>
OUTPUT/INPUT THROUGHPUT RATIO	<input type="text"/>	<input type="text"/>
AVERAGE TRANSACTION PROCESS LATENCY	<input type="text"/>	<input type="text"/>

FIG. 25

2400

USER CONDITION INPUT DIALOG

USER IDENTIFICATION CONDITION

ACCESS SOURCE IP ADDRESS	<input type="text"/>	<input type="text"/>
ACCESS DESTINATION IP ADDRESS	<input checked="" type="checkbox"/>	a100
...	<input type="text"/>	<input type="text"/>

SERVER TYPE      IP ADDRESS

WEB SERVER	<input checked="" type="checkbox"/>	a100
AP SERVER	<input type="checkbox"/>	<input type="text"/>
DB SERVER	<input type="checkbox"/>	<input type="text"/>